REMARKS

I. Status of Claims

Claims 21-24 have been canceled, which leaves claims 1-20 currently pending in the application. This Amendment addresses each point of objection and rejection raised by the Examiner. Favorable reconsideration is respectfully requested.

II. Objection to the Specification

The Applicants have amended claims 3, 11 and 16 to provide proper antecedent basis for elements recited therein. Support for the Applicants' amendments can be found on page 11, lines 2-4 of Applicants' originally filed application.

III. Rejections under 35 U.S.C. §101

Method claims 21-23 are rejected for allegedly providing steps that do not result in a physical transformation or a tangible result. The Applicants have canceled claims 21-24, as indicated above. Accordingly, withdrawal of the rejection is respectfully requested.

IV. Rejections under 35 U.S.C. §102(b)

Claims 1-20 are rejected under 35 U.S.C. §102(b) as being unpatentable by Leporini (U.S. Patent Application Publication No. 2003/0110382). These rejections are respectfully traversed.

The Applicants respectfully note that the rejection under 35 U.S.C. § 102(b) is improper. In particular, the United States filing date of the Applicants' application is

December 01, 2003. However, the publication date of the Leporini is June 12, 2003, which predates the Applicants' U.S. filing date by only 5 months. Accordingly, Leporini was not published more than year before the Applicants' U.S. filing date and the rejection under 35 U.S.C. § 102(b) is improper.

Furthermore, the Examiner has rejected claims 1-19 under 35 U.S.C. § 102(b) based upon alleged public use or sale. However, the Examiner has not provided any clear evidence of public use or sale of the invention more than one year prior to the Applicants' U.S. filing date. Accordingly, the Applicants' respectfully request the Examiner to provide evidence of this alleged public use or sale.

Nonetheless, the filing date of Leporini (November 13, 2002) predates the Applicants' U.S. filing date (December 01, 2003). Therefore, Leporini is prior art under 35 U.S.C. § 102(e) and the Applicants' have considered the Examiner's arguments, accordingly.

Independent claims 1 and 9 each recite a hybrid digital broadcasting receiver for reproducing digital multimedia data comprising a broadcast receiving module including a first demultiplexer for demultiplexing said demodulated digital broadcasting data stream, and selecting and extracting digital broadcasting data corresponding to a program selected by a user and a second demultiplexer for demultiplexing a digital multimedia data stream which includes a multiplexed plurality of compressively encoded digital multimedia data; and a decoding section for decoding digital broadcasting data output from said broadcast receiving module and digital multimedia data output from said second demultiplexer. The Examiner

proposes that Leporini discloses a second demultiplexer for demultiplexing a digital multimedia data stream. However, the Applicants respectfully disagree.

The Examiner refers to a hard disk (2100) disclosed by Leporini as being equivalent to a second demultiplexer, as disclosed by the Applicants. However, the hard disk (2100) merely stores broadcasted data received and generated by the receiver/decoder (2000) (see paragraph [0199]). Leporini does not disclose, or remotely suggest, that the hard disk (2000) is capable of demultiplexing streamed digital multimedia file. Moreover, since the hard disk is not capable of demultiplexing signals, all signals must be demultiplexed by a single demultiplexer (2010), which is contrary to the Applicants' teachings of providing a first and a second demultiplexer. Specifically, the receiver/decoder disclosed by Leporini may inadequately reproduce digital multimedia data other than digital multimedia broadcasting data in the decoder section of the receiver since the receiver must first decrypt demultiplexed digital broadcasting data at the conditional access section before decoding the data at the decoding section. Therefore, Leporini fails to teach, show, or suggest a broadcast receiving module including a first demultiplexer for demultiplexing said demodulated digital broadcasting data stream, and selecting and extracting digital broadcasting data corresponding to a program selected by a user and a second demultiplexer for demultiplexing a digital multimedia data stream which includes a multiplexed plurality of compressively encoded digital multimedia data; and a decoding section for decoding digital broadcasting data output from said broadcast receiving module and digital multimedia data output from said second

demultiplexer. Reconsideration and withdrawal of the rejection are respectfully requested.

Regarding claims 2-14, the Applicants note that each ultimately depends from independent claims 1 and 9, respectively, which define over the prior art for at least the reasons discussed in detail above. Therefore, claims 2-14 also define over the prior art and reconsideration and withdrawal of the rejections are respectfully requested.

Claim 15, as amended, recites an integrated chip having at least one surface for processing digital broadcasting data, the integrated chip including a receiving section disposed on said at least one surface of said integrated chip for receiving and demodulating a digital broadcasting data stream which includes multiplexed and transferred digital broadcasting data packets and conditional access information packets for a plurality of programs; an error correcting section disposed on said at least one surface of said integrated chip for correcting any error in said demodulated digital broadcasting data stream; a demultiplexer disposed on said at least one surface of said integrated chip for separating said conditional access information packets and digital broadcasting data packets for a program selected by a user from said demodulated digital broadcasting data stream; and a conditional access section disposed on said at least one surface of said integrated chip and in electrical communication with said demultiplexer for detecting conditional access information from said conditional access information packets and decrypting said separated digital broadcasting data packets using said conditional access information.

Upon considering the prior art, the Applicants note that Leporini merely discloses a receiver/decoder. Leporini is silent as to a receiving and/or decoding module constructed as an integrated chip. Further, Leporini fails to disclose a conditional access section being positioned in the broadcasting receiving module, as claimed in at least one exemplary embodiment recited in claim 15.

Leporini discloses a conventional digital multimedia broadcasting receiver, in which all programs included in a transport stream received by a digital media broadcasting (DMB) module are transferred to the decoder module. As a result, data transmission is performed through a Packet Assembler/Disassembler (PAD), which is unfavorable because a large amount of power is required in order to operate the PAD. Furthermore, since Leporini is silent as to separating the decoder module and broadcasting receiving module including a conditional access section, the entire module including the conditional access section must be replaced, when only the decoding section needs to be changed. Therefore, Leporini fails to teach, show, or suggest an integrated chip having at least one surface for processing digital broadcasting data, the integrated chip including a receiving section disposed on said at least one surface of said integrated chip for receiving and demodulating a digital broadcasting data stream which includes multiplexed and transferred digital broadcasting data packets and conditional access information packets for a plurality of programs; an error correcting section disposed on said at least one surface of said integrated chip for correcting any error in said demodulated digital broadcasting data stream; a demultiplexer disposed on said at least one surface of said integrated chip

for separating said conditional access information packets and digital broadcasting data packets for a program selected by a user from said demodulated digital broadcasting data stream; and a conditional access section disposed on said at least one surface of said integrated chip and in electrical communication with said demultiplexer for detecting conditional access information from said conditional access information packets and decrypting said separated digital broadcasting data packets using said conditional access information. Reconsideration and withdrawal of the rejection are respectfully requested.

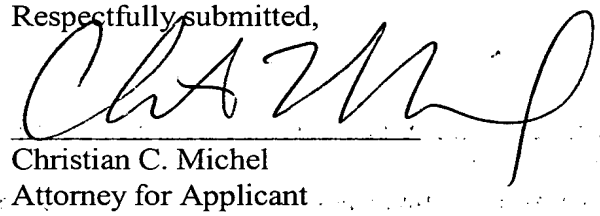
Regarding claims 16-20, the Applicants respectfully note that each ultimately depends from independent claim 15, which defines over the prior art for at least the reasons discussed in detail above. Therefore, claims 16-19 also define over the prior art and reconsideration and withdrawal of the rejections are respectfully requested.

CONCLUSION

In view of the above, it is believed that the above-identified application is in condition for allowance, and notice to that effect is respectfully requested. Should the Examiner have any questions, the Examiner is encouraged to contact the undersigned at the telephone number indicated below.

Should any/additional fees be required, the Director is hereby authorized to charge the fees to Deposit Account No. 18-2220.

Respectfully submitted,



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